

# Marvell 美满电子 22 届实习

## Essential technology, done right

关于我们: Marvell 成立于 1995 年, 总部位于美国加州硅谷, 全球员工近 6000 人, 拥有全球专利 10000 余项。Marvell 在美国、中国、印度、德国、以色列设有研发中心。Marvell 中国总部位于上海张江, 并在北京、南京、成都设有研发中心。

在 Marvell, 我们坚信, 基础设施是社会进步的重要推动力。我们认为, 执行力与创新力同等重要。建立紧密和广泛的合作是推动技术进步的基础。因此, Marvell 的理念就是, 与您携手并进。广泛合作为我们打下坚实的基础, 让一切进步皆有可能。

公司网址: [www.marvell.com/](http://www.marvell.com/)

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### Software/Firmware (Shanghai/Chengdu)

#### Responsibility:

As a Firmware Develop Engineer(intern) in Marvell Chengdu SSD Firmware Design team, your main responsibility will be:

- 1、Familiar with Marvell SSD SoC, develop hardware abstraction layer, implement unit test cases for the APIs and write related documentations.
- 2、Develop SSD firmware features for both client SSD and Data Center SSD.
- 3、Support internal firmware teams and external customers to resolve problems.
- 4、Work with hardware design team to improve SSD SoC design.

#### Requirements:

- 1、MS in EE/CE/CS.
- 2、Familiar with C/C++ programming.
- 3、Assembly/Python/Shell is a plus.
- 4、Experience in embedded firmware development, storage related experience is a plus.
- 5、Willingness to take on challenges and learn new technologies
- 6、Solid Verbal and written English communication skills.
- 7、Ability to work within a team environment as well as ability to work independently.
- 8、Self motivated, conscientious, patient, clear headed and hardworking.

## System Design(Software Test) (Shanghai/Chengdu)

### Responsibility:

This is an entry level, software engineering position. Development of software components for automotive Ethernet products, focusing on compliance and EMC for PHY and switch systems, which includes developing GUI based software for both FPGA verification system and silicon product, script-based automation test suit for function/compliance validation, and MATLAB/Python based algorithm implementation for protocol validation and prototyping. Validation of automotive products both in protocol level and system level.

### Qualifications:

- 1、MS in EE/CE/CS and related areas.
- 2、Must have good C/C++ programming skill.
- 3、Hands on experience with ARM architecture, embedded RTOS, firmware development and SoCs debugging is preferred.
- 4、Hands on experience with massive data processing and script language (e.g. Matlab, Python) is preferred.
- 5、Hands on experience with automotive Ethernet protocols and industry standards is preferred.
- 6、Knowledge of signal processing is a plus.
- 7、Knowledge of GUI development is a plus.
- 8、Good communication skills in English, written and spoken.
- 9、Must be flexible, proactive and able to work in a team.

## FPGA Engineer (Shanghai)

### Responsibility:

- 1、Work with global architecture and designers to get a full deep insight on the IC products(Optical/Copper/Automotive PHY, Switch products)
- 2、FPGA logic design is developing FPGA/Emulator based IP regarding cutting-edge Ethernet Protocol(IEEE802.3/IEEE802.1)
- 3、FPGA verification design is responsible for IC prototyping, pre-silicon & post-silicon validation
- 4、FPGA hardware design develops for Hardware tools, schematic design and PCB layout
- 5、Design integral system tool development, research and complete architecture level products

### Qualifications:

- 1、MS in EE or related.
- 2、Must have solid knowledge and skill with Verilog/SystemVerilog logic design
- 3、Skilled in FPGA tools is preferred
- 4、Skilled in UVM/VMM is a plus
- 5、Familiar with schematic and PCB layout is a plus

- 6、Familiar with Emulator(Palladium/Veloce) is a plus
- 7、Must have good knowledge with basic electronic circuit design.
- 8、Must be willing to work in a multi-tasking, fast pace, team oriented working environment
- 9、Must be able to quickly absorb different kinds of knowledge and apply them in the works
- 10 、 Working experience on logic/FPGA design, board level hardware design, or software development are preferred

### Design for Test Engineer (Shanghai)

#### Responsibility:

DFT design engineer focus on DFT design and verification

#### Qualifications:

- 1、BS (MS preferred) in microelectronics, electrical engineering who will graduate next year.
- 2、Familiar with RTL and simulation.
- 3、STA and timing experience is a plus.
- 4、Must be able to communicate in both written and spoken English
- 5、Good team work spirit and communication skill.
- 6、Able to work 3 days a week.

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