**澜起科技（上海）有限公司校园招聘**

各位同学，

您好。澜起科技作为IC产业一颗耀眼的新星，以电子芯片研发设计为主要业务，公司目前正处于快速发展阶段，规模已达500多人。我司不仅拥有良好的成长平台与发展机会，有竞争性的市场薪资福利机制，而且我司奉行人才至上原则，致力于让每一位员工在我司愉快的工作、快乐成长。以下职位针对广大优秀应届生开放，欢迎各位同学踊跃申请。

澜起科技诚觅英才，快点加入我们吧！

***职位简介：***

**JOB TITLE: IC Design Engineer(RF)**

**职位：IC设计工程师(射频)**

**JOB DESCRIPTION:**

* RF/analog IC design for tuners and receivers for multi-standard TV’s and other wireless systems;
* Design and layout of IC blocks, such as LNA, mixer, VGA/PGA, LPF/BPF, VCO, crystal oscillator, PLL, ADC and DAC;
* Simulation of RF and analog circuits and systems in Spectre/SpectreRF and Matlab;
* Testing and characterization of IC blocks and chips in lab, and ATE and field environments;
* IC process and package evaluation including device modeling and PDK.

**QUALIFICATION:**

* MS or PhD in electric and electronic engineering;
* Understanding receiver architectures for multi-standard TV products and other wireless systems;
* Hands-on experiences in design of IC blocks to chip top levels in deep submicron CMOS technologies;
* Proficient with simulation tools (Spectre/SpectreRF) and oversight of layout design;
* Demonstrated ability for characterization of block and chip performances in lab and ATE environments;
* Strong communication skills and also excellence as team player.

**JOB TITLE: Signal Processing Engineer**

**职位：信号处理算法工程师**

**JOB DESCRIPTION:**

* Advanced communication system R&D;
* Signal processing algorithm implementation for data communication;
* Participate in projects and work with hardware & software engineers for product development, debug & test support.

**QUALIFICATIONS:**

* Ph. D or MS of related background;
* Top student with strong self motivation; experienced candidate should have working knowledge in the area of digital communication;
* All candidates are expected to have in depth knowledge in the area of digital communication;
* Good communication skill, team work spirit, strong self-motivated;
* Hands-on with Matlab, C/C++, etc.

**JOB TITLE: Video Architecture Engineer**

**职位：视频架构工程师**

**JOB DESCRIPTION:**

* Work in the area of video architecture, algorithm, and software development;
* Develop video/image compression/decompression/processing algorithm, including lossy and lossless;
* Build software models for algorithm with C/C++/SystemC, and assist with hardware design and verification.

**QUALIFICATION:**

* Ph. D or MS in signal processing, applied mathematics, computer science, electronic engineering and related discipline with good mathematical knowledge;
* Strong experience in algorithms development in one or some of the following signal processing technologies: digital video hardware or software development; image processing/multimedia design; real-time and embedded system design;
* Experience in some of related fields: video compression; video decompression; image compression; image decompression, video processing, image processing, etc.;
* Familiar with video codec standards such as MPEG2 / H.264 / HEVC / VP9, etc.;
* Strong software skills in C/C++/SystemC.;
* Candidates with academic background or ASIC design experience are a big plus to this position.

**JOB TITLE: IC Design Engineer（Analog)**

**职位：IC设计工程师（模拟）**

**JOB DESCRIPTION:**

* Design, evaluate and verify high speed CMOS analog circuits;
* Oversee layout and verification activities which include floor plan, LVS and DRC;
* Generate design spec and write design review document;
* Engineering lab testing and chip debug.

**QUALIFICATION:**

* MS in electric and electronic engineering;
* Good fundamental in device physics, analysis and design of analog / mixed-signal circuits;
* Experience in Verilog, AHDL and/or Matlab;
* Ability to do layout floor plan and provide verification/debugging guidance;
* Solid knowledge of EDA design tools. (Analog artist, spectre, HSPICE and nc-verilog ...);
* Design experience in any of the following areas is preferred: PLL,  high-speed I/O’s, transceiver, LV-LP analog design.

**职位申请方式：**

各位同学可将简历投递至招聘联络人邮箱。其他职位请登陆前程无忧、智联招聘等招聘网站进行线上申请：

前程无忧招聘界面：<http://search.51job.com/list/co,c,408570,000000,10,1.html#syzw>

***公司介绍：***

澜起科技集团有限公司（简称“澜起”），是一家专注于为家庭娱乐和云计算市场提供模拟与混合信号半导体解决方案的全球芯片供应商。

公司拥有非常完善的研发设计平台，其中包含了射频、模拟前端、数字信号处理以及高速接口等自主研发的功能模块，能够快速、灵活地进行模块化设计，研发出高性能、低功耗的芯片产品。

在家庭娱乐领域，澜起利用此平台为机顶盒客户提供了全方位、高集成度芯片的解决方案，其中软件方案更是为客户贴身定制。这些方案优化了机顶盒的信号处理能力，尤其适用于新兴市场的严苛工作环境。

在云计算领域，澜起致力于提供高性能、低功耗的内存接口解决方案，以满足内存密集型服务器的应用需求。通过模块化的研发平台，澜起能够实现持续创新、快速高效的产品设计，不断推出更为优化的整体解决方案，以满足客户日异月新的需求。

**我们的团队：**

澜起科技集团下属上海、苏州、杭州、台湾、香港、澳门以及美国等多家分公司，目前共有员工500多人。

**我们的4大优势：**

1. 工作环境与作风：自由、宽松、友好的工作环境；务实、执着、专注的工作作风；公司注重员工工作与生活的平衡，为员工提供兴趣发展的平台，鼓励员工健康生活，快乐工作。

2. 学习和成长平台：公司集模拟、射频、数字、数模混合、信号处理等多领域的行业专家以及优秀开发团队，为员工提供专业深入的带教和辅导，帮助员工迅速成长并不断提高专业研发水平；产品研发涉及从前端到后端各个领域，员工在术有专攻的基础上，有机会和平台不断拓宽、积累知识和经验。成立至今，已申请中国和美国专利70多项，充分展示了公司科技研发团队的实力。

3. 职业路径与发展空间：公司为员工设计了清晰可行的专业及管理双通道的职业发展路径，在发挥员工优势的基础上，更关注员工工作与兴趣的结合，给予员工更多不同的职业发展选择。

4. 薪酬与未来前景：除了为员工提供有竞争力的薪酬与多样化的福利，公司还根据员工的发展潜力以及绩效，提供股票与期权的奖励。员工可以从中享受到更为丰厚的回报。

**详情请见我司官网：**[www.montage-tech.com](http://www.montage-tech.com)

***联系方式：***

**人力资源部：**

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